L Number	Hits	Search Text	DB	Time stamp
3	696	(mass adj storage) with (magneto adj	USPAT;	2004/06/07 18:06
		optical) and @ad < "20010829"	US-PGPUB;	
		_	EPO; JPO;	
			DERWENT;	
			IBM_TDB	0004/06/07 35 44
4	13	((mass adj storage) with (magneto adj	USPAT;	2004/06/07 15:44
		optical) and @ad < "20010829") and (usb with	US-PGPUB; EPO; JPO;	
		interface)	DERWENT;	
			IBM TDB	
5	9	(((mass adj storage) with (magneto adj	USPAT;	2004/06/07 15:44
		optical) and @ad < "20010829") and (usb with	US-PGPUB;	
		interface)) and (controller)	EPO; JPO;	
			DERWENT;	
			IBM_TDB	
6	12	((mass adj storage) with (magneto adj	USPAT;	2004/06/07 16:24
		optical) and @ad < "20010829") and (set adj	US-PGPUB;	
		top adj box)	EPO; JPO;	
			DERWENT; IBM TDB	
7	56	(mass adj storage) with (controller) and	USPAT;	2004/06/07 16:56
'	30	(set adj top adj box) and @ad < "20010829"	US-PGPUB;	
		( aa) bop aa) bon, and baa 1 bootsob	EPO; JPO;	
			DERWENT;	
			IBM_TDB	
8	23		USPAT;	2004/06/07 18:03
		controller) and (disk same head same	US-PGPUB;	
		actuator and servo) and @ad < "20010829"	EPO; JPO;	
			DERWENT;	
	222	(mage add grownes) and (digk game head game	IBM_TDB USPAT;	2004/06/07 17:23
9	333	(mass adj storage) and (disk same head same actuator and servo) and @ad < "20010829"	US-PGPUB;	2004/08/07 17.23
		actuator and servo, and wad < 20010829	EPO; JPO;	
			DERWENT;	
			IBM TDB	
10	66	(mass adj storage) and (disk same head same	USPAT;	2004/06/07 17:24
		actuator and (servo adj controller)) and @ad	US-PGPUB;	
		< "20010829"	EPO; JPO;	
			DERWENT;	
11	,	(mana adi atawasa) sama (mamayu adi	<pre>IBM_TDB USPAT;</pre>	2004/06/07 18:03
11	3	(mass adj storage) same (memory adj controller) and (disk same head same	US-PGPUB;	2004/00/07 10:03
		actuator and servo) and @ad < "20010829"	EPO; JPO;	
		accuator and Bervey and oud a Bervey	DERWENT;	
			IBM_TDB	
12	64	((mass adj storage) with (memory adj	USPAT;	2004/06/07 18:07
		controller)) and @ad < "20010829"	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
	1115	(711/112).CCLS.	IBM_TDB USPAT;	2004/06/04 11:54
-	1113	(/11/114/.0000.	US-PGPUB;	2501/05/04 11.54
			EPO; JPO;	
	-		DERWENT;	
			IBM_TDB	
-	5326	(710/8,22-28,100,113,305,306,308,309,311,313)	; TÆREID	2004/06/04 11:54
			US-PGPUB;	
1			EPO; JPO;	
			DERWENT;	
	E205	///711/112) CCIC \	<pre>IBM_TDB USPAT;</pre>	2004/06/04 11:55
-	5395	(((711/112).CCLS.) ((710/8,22-28,100,113,305,306,308,309,311,313)	•	2004/00/04 11:55
		and @ad < "20010830"	EPO; JPO;	
			DERWENT;	
			IBM TDB	
-	2969	((((711/112).CCLS.)	USPAT;	2004/06/04 11:55
		((710/8,22-28,100,113,305,306,308,309,311,313)	USCHREDUB;	
		and @ad < "20010830") and disk	EPO; JPO;	
			DERWENT;	
L			IBM_TDB	

((1710/4/32-28-100,113,305,306,309,310,313) and ead < "2010830") and (disk storage)  = 2821 ((((1711/112).CCLS.)		1100	((((711/112).CCLS.)	USPAT;	2004/06/04 11:56
and sad < "20010830") and (disk storage)  [PFO; JPO; DERMENT; TIM TDD [CT07/8, 22-28, 100, 113, 305, 306, 308, 309, 311, 313] INCREMING: and sead < "2001080") and (disk storage)  [PFO; JPO; DOWNOOD AND AND AND AND AND AND AND AND AND AN	_	4190			2004/06/04 11:36
Comparison					
1887 TD			and wad C 20010030 / and (disk beelage)		
2821   (((((711/112).CCLS.) (1/07/8,22-28,100,113,305,306,308,309,311,313   USPAT) (1/07/8,22-28,100,113,305,306,308,30					
	_	2821	(((((711/112).CCLS.)		2004/06/04 11:57
and (memory and controller and bus)  - 2282 ((((((((711/112).CCLS.) ((710/8,22-28,100,113,305,306,308,309,311,313) LECHEMBLE ((710/8,22-28,100,113,305,306,308,309,31		2021		•	
and (memory and controller and bus)  - 2282 ((((((1711/12).CCLS.) (170/6,22-28,100,113,305,306,308,309,311,313) (SPAT; (710/6,22-28,100,113,305,306,308,309,311,313) (SPAT; (170/6,22-28,100,113,305,306,308,309,311,313) (((((((1711/112).CCLS.) ((710/6,22-28,100,113,305,306,308,309,311,313) ((((((((1711/112).CCLS.) ((((((((1711/112).CCLS.) ((((((((((((((((((((((((((((((((((((					
2022   (((((((T11/112).CCLS.) ((T10/8,22-28,100.113,305,306,308,309,311,313)   SEMBRIUS: and wad < "20010830") and (disk storage!) and (memory and controller and bus) and processor (((((((((((((((((((((((((((((((((((					
2262   (((((((((((((((((((((((((((((((			,		
((710/8, 22-28, 100, 113, 305, 306, 308, 309, 311, 313)   ISSNESSAMS, emoty and controller and bus) and processor   ((((((((1711/112).CCLS.)) (((((((((1711/112).CCLS.)) (((((((((((1711/112).CCLS.)) (((((((((((((((((((((((((((((((((((	-	2282	((((((711/112).CCLS.)	_	2004/06/04 19:17
and @ad < "2010839") and (disk storage) and (memory and controller and bus) and processor (((((((((1/11/12).CLS.) ((((((((((((((((((((((((((((((((((((				UCCHRGP)UB;	, ,
and (memory and controller and bus)) and processor and low memory and controller and bus) and grocessor and low processor) and bus and grocessor) and arbitrats) and grocessor) and bus and grocessor) and arbitrats) and grocessor) and grocessor				-	
Deprocessor   1080   ((((((((((((((((((((((((((((((((((					
((710/8,22-28,100,113,305,306,308,309,311,313)   INSPERIENCES   and wad < "20010830") and (disk storage)   DERMENT; TRM TDB   Controller and bus) and processor) and DMA   controller and bus) and processor) and man dead < "20010830") and (disk storage)   DERMENT; TRM TDB   Controller and bus) and processor) and DMA   and PCI and SCSI and USB   DERMENT; TRM TDB   Controller and bus)   and processor) and DMA   and PCI and SCSI and USB   DERMENT; TRM TDB   Controller and bus)   and processor) and DMA   and PCI and SCSI and USB   DERMENT; TRM TDB   Controller and bus)   and processor) and DMA   and PCI and SCSI and USB   DERMENT; TRM TDB   Controller and bus)   and   DERMENT; TRM TDB   Controller and bus)   Controller and bus   Controller an			processor	IBM TDB	
((710/8,22-28,100,113,305,306,308,309,311,313)   INSPERIENCES   and wad < "20010830") and (disk storage)   DERMENT; TRM TDB   Controller and bus) and processor) and DMA   controller and bus) and processor) and man dead < "20010830") and (disk storage)   DERMENT; TRM TDB   Controller and bus) and processor) and DMA   and PCI and SCSI and USB   DERMENT; TRM TDB   Controller and bus)   and processor) and DMA   and PCI and SCSI and USB   DERMENT; TRM TDB   Controller and bus)   and processor) and DMA   and PCI and SCSI and USB   DERMENT; TRM TDB   Controller and bus)   and processor) and DMA   and PCI and SCSI and USB   DERMENT; TRM TDB   Controller and bus)   and   DERMENT; TRM TDB   Controller and bus)   Controller and bus   Controller an	_	1080		USPAT;	2004/06/04 14:37
and wad < "20010830") and (disk storage) and (memory and controller and bus)) and processor) and DMA (((((((('1/11/12).CLS.) (('(10/8,22-28,100,113,305,306,308,309,311,313) INJENSEMB); and wad < "20010830") and (disk storage)) and (memory and controller and bus)) and processor) and DMA) and processor) and DMA) and processor) and DMA) and (controller with ((memory near interface) with ((storage disk interface))) and (memory and controller and bus)) and processor) and DMA) and (controller with (('(10/8,22-28,100,113,305,306,308,309,311,313) INJENSEMB, and wad < "20010830") and (disk storage)) and (memory near interface) with ((storage disk interface)) (('(((('(('(1711/12).CCLS.) (('(('(('(1711/12).CCLS.) (('(('(('(('(1711/12).CCLS.) (('(('(('(('(1711/12).CCLS.) (('(('(('(('(711/12).CCLS.) (('(('(('(('(711/12).CCLS.) (('(('(('(('(711/12).CCLS.) (('(('(('(('(711/12).CCLS.) (('(('(('(('(711/12).CCLS.) (('(('(('(('(711/12).CCLS.) (('(('(('('(711/12).CCLS.) (('(('(('(('(711/12).CCLS.) (('(('(('(('('(711/12).CCLS.) (('(('(('(('(711/12).CCLS.) (('(('(('(('(711/12).CCLS.) (('(('(('(('(711/12).CCLS.) (('(('(('(('(711/12).CCLS.) (('(('(('(('(711/12).CCLS.) (('(('(('('(711/12).CCLS.) (('(('(('('(711/12).CCLS.) (('(('(('('(711/12).CCLS.) (('(('(('('(711/12).CCLS.) (('(('(('('(711/12).CCLS.) (('(('(('('(711/12).CCLS.) (('('('('('('('(111.12).CCLS.) (('('('('('('('('('('('('('('('('('('(				LECHREDUB;	·
and (memory and controller and bus)) and processor) and DMA (((((((((T/11/12).CCLS.) (((((((((T/11/12).CCLS.) (((((((((T/11/12).CCLS.) ((((((((((T/11/12).CCLS.) ((((((((((T/11/12).CCLS.) ((((((((((((((((((((((((((((((((((((			and @ad < "20010830") and (disk storage))		
Control   Cont		ļ		DERWENT;	
((710/8,22-28,100,113,305,306,308,309,311,313   IECHENIAME; and @ad < "20010830") and (disk storage)   EPO; JPO; DERWENT; and @ad < "20010830") and PCI and SCSI and USB   USPAT; USPAT; and @ad < "20010830") and (disk storage)   EPO; JPO; DERWENT; and @ad < "20010830") and (disk storage)   EPO; JPO; DERWENT; and @ad < "20010830") and (disk storage)   EPO; JPO; DERWENT; and @ad < "20010830") and (disk storage)   EPO; JPO; DERWENT; and @ad < "20010830") and (disk storage)   EPO; JPO; DERWENT; and @ad < "20010830") and (disk storage)   EPO; JPO; DERWENT; and @ad < "20010830") and (disk storage)   EPO; JPO; DERWENT; and @ad < "20010830") and (disk storage)   EPO; JPO; DERWENT; and @ad < "20010830") and (disk storage)   EPO; JPO; DERWENT; and @ad < "20010830") and (disk storage)   EPO; JPO; DERWENT; and @ad < "20010830") and (disk storage)   EPO; JPO; DERWENT; and @ad < "20010830") and (disk storage)   EPO; JPO; DERWENT; and @ad < "20010830") and (disk storage)   EPO; JPO; DERWENT; and @ad < "20010830") and (disk storage)   EPO; JPO; DERWENT; and @ad < "20010830") and (disk storage)   EPO; JPO; DERWENT; and @ad < "20010830") and (disk storage)   EPO; JPO; DERWENT; and @ad < "20010830") and (disk storage)   EPO; JPO; DERWENT; and @ad < "20010830") and (disk storage)   EPO; JPO; DERWENT; and @ad < "20010830") and (disk storage)   EPO; JPO; DERWENT; and @ad < "20010830") and (disk storage)   EPO; JPO; DERWENT; and @ad < "20010830") and (disk storage)   EPO; JPO; DERWENT; and @ad < "20010830") and (disk storage)   EPO; JPO; DERWENT; and @ad < "20010830") and (disk storage)   EPO; JPO; DERWENT; and @ad < "20010830") and (disk storage)   EPO; JPO; DERWENT; and @ad < "20010830") and (disk storage)   EPO; JPO; DERWENT; and @ad < "20010830") and (disk storage)   EPO; JPO; DERWENT; and @ad < "20010830") and (disk storage)   EPO; JPO; DERWENT; and @ad < "20010830") and (disk storage)   EPO; JPO; DERWENT; and @ad < "20010830") and (disk storage)   EPO; JPO; DERWENT; and @ad < "20010830") and (disk storage)   EPO; JPO; DERWENT;			processor) and DMA	IBM TDB	
and @ad < "20010830") and (disk storage)) and (memory and controller and bus)) and processor) and DMA) and PCI and SCSI and USB (((((((I(II)/12).CLS.)) (((III)/12).CLS.)) (((III)/12).CLS.)) (((III)/12).CLS.)) (((III)/12).CLS.)) (((III)/12).CLS.)) (((III)/12).CLS.)) (((III)/12).CLS.)) (((memory and controller and bus)) and processor) and DMA) and (controller with ((storage)) and (memory and controller and bus)) and processor) and DMA) and (isk storage)) and (memory and controller and bus) and (memory and controller and bus) and processor) and DMA) and (memory near bus) (((((((III)/112).CLS.)) ((((((III)/112).CLS.)) ((((((III)/112).CLS.)) (((((((III)/112).CLS.)) (((((((III)/112).CLS.)) ((((((((III)/112).CLS.)) (((((((((III)/112).CLS.)) (((((((((((III)/112).CLS.)) ((((((((((((((II)/112).CLS.)) (((((((((((((((((((((((((((((((((((	-	26	(((((((((711/112).CCLS.)	USPAT;	2004/06/04 14:37
and (memory and controller and bus)) and processor) and DMA) and PCI and SCSI and USB (((((((((((((((((((((((((((((((((((			((710/8,22-28,100,113,305,306,308,309,311,313)	LUSC-IRGEP)U/B;	
and (memory and controller and bus)) and processor) and DMA) and PCI and SCSI and USB (((((((((((((((((((((((((((((((((((				EPO; JPO;	
Processor  and DMA) and PCI and SCSI and USB			and (memory and controller and bus)) and		
2 ((((((((((((((((((((((((((((((((((((			processor) and DMA) and PCI and SCSI and USB	IBM TDB	
and @ad < "20010830") and (disk storage)) and (memory and controller and bus)) and processor) and DMA) and (controller with ((memory near interface) with ((storage disk prightstall/meal* (fuffishrace)) and @ad < "20010830") and (disk storage)) and (memory and controller and bus)) and processor) and DMA) and (memory near bus) ((((((((1(711/112).CCLS.)) (((710/8,22-28,100,113,305,306,308,309,311,313) and @ad < "20010830") and (disk storage)) and (memory and controller and bus)) and processor) and DMA) and (memory near bus) disk((disk/disk/dish12).CCLS.) (((710/8,22-28,100,113,305,306,308,309,311,313) and @ad < "20010830") and (disk storage)) and (memory and controller and bus)) and processor) and DMA) and (memory near bus) disk((disk/dish12).CCLS.) (((710/8,22-28,100,113,305,306,308,309,311,313) and @ad < "20010830") and (disk storage)) and (memory and controller and bus)) and processor) and DMA) and (memory near bus) disk((disk(dish212)andCusbEtr as) and (disk disc)) and arbitrat\$3) and (PCI  12 disk((\$C\$112810CLSSE) (((710/8,22-28,100,113,305,306,308,309,311,313) and @ad < "20010830") and (disk storage)) and (memory and controller and bus)) and processor) and DMA) and (memory near bus) and (disk disc)) and arbitrat\$3) and (PCI  12 disk((\$C\$112810CLSSE) (((710/8,22-28,100,113,305,306,308,309,311,313) and @ad < "20010830") and (disk storage)) and (memory and controller and bus)) and processor) and DMA) and (memory near bus) and (disk disc)) and arbitrat\$3) and (PCI  18 disk((\$C\$7113610ESBECLS.) (((710/8,22-28,100,113,305,306,308,309,311,313) and @ad < "20010830") and (disk storage)) and (memory and controller and bus)) and processor) and DMA) and (memory near bus) and (disk disc)) and arbitrat\$3) and (PCI ((710/8,22-28,100,113,305,306,308,309,311,313) and @ad < "20010830") and (disk storage)) and (memory and controller and bus)) and processor) and PCI and USB and SCSI and ((710/8,22-28,100,113,305,306,308,309,311,313) and @ad < "20010830") and (disk storage)) and (memory and controller and bus)) and processor) an	-	2			2004/06/04 12:07
and @ad < "20010830") and (disk storage)) and (memory and controller and bus)) and processor) and DMA) and (controller with ((memory near interface) with ((storage disk prightstall/meal* (fuffishrace)) and @ad < "20010830") and (disk storage)) and (memory and controller and bus)) and processor) and DMA) and (memory near bus) ((((((((1(711/112).CCLS.)) (((710/8,22-28,100,113,305,306,308,309,311,313) and @ad < "20010830") and (disk storage)) and (memory and controller and bus)) and processor) and DMA) and (memory near bus) disk((disk/disk/dish12).CCLS.) (((710/8,22-28,100,113,305,306,308,309,311,313) and @ad < "20010830") and (disk storage)) and (memory and controller and bus)) and processor) and DMA) and (memory near bus) disk((disk/dish12).CCLS.) (((710/8,22-28,100,113,305,306,308,309,311,313) and @ad < "20010830") and (disk storage)) and (memory and controller and bus)) and processor) and DMA) and (memory near bus) disk((disk(dish212)andCusbEtr as) and (disk disc)) and arbitrat\$3) and (PCI  12 disk((\$C\$112810CLSSE) (((710/8,22-28,100,113,305,306,308,309,311,313) and @ad < "20010830") and (disk storage)) and (memory and controller and bus)) and processor) and DMA) and (memory near bus) and (disk disc)) and arbitrat\$3) and (PCI  12 disk((\$C\$112810CLSSE) (((710/8,22-28,100,113,305,306,308,309,311,313) and @ad < "20010830") and (disk storage)) and (memory and controller and bus)) and processor) and DMA) and (memory near bus) and (disk disc)) and arbitrat\$3) and (PCI  18 disk((\$C\$7113610ESBECLS.) (((710/8,22-28,100,113,305,306,308,309,311,313) and @ad < "20010830") and (disk storage)) and (memory and controller and bus)) and processor) and DMA) and (memory near bus) and (disk disc)) and arbitrat\$3) and (PCI ((710/8,22-28,100,113,305,306,308,309,311,313) and @ad < "20010830") and (disk storage)) and (memory and controller and bus)) and processor) and PCI and USB and SCSI and ((710/8,22-28,100,113,305,306,308,309,311,313) and @ad < "20010830") and (disk storage)) and (memory and controller and bus)) and processor) an			((710/8,22-28,100,113,305,306,308,309,311,313)	USCIEGP)UB;	
Processor   and DMA) and (controller with ((memory near interface) with ((storage disk peripherall/mear.cuterface))   ((10/8, 22-28,100,113,305,306,308,309,311,313)   USPAT;   USPAT	1		and @ad < "20010830") and (disk storage))	EPO; JPO;	
(memory near interface) with ((storage disk pf4tjbhefall/meal.cffserace))			and (memory and controller and bus)) and	DERWENT;	
(memory near interface) with ((storage disk pf4tjbhefall/meal.cffserace))			processor) and DMA) and (controller with	IBM TDB	
C(710/8,22-28,100,113,305,306,308,309,311,313)   LECHERDIUB; and @ad < "20010830") and (disk storage) and (memory and controller and bus) and processor) and DMA) and (memory near bus)   LEPO; JPO; and (memory and controller and bus)   LEPO; JPO; LEPO; JPO; and (memory and controller and bus)   LEPO; JPO; LEPO; JPO; and (memory and controller and bus)   LEPO; JPO; LEPO; JPO; LEPO; JPO; and (memory and controller and bus)   LEPO; JPO; LEPO; JPO; LEPO; JPO; and (memory and controller and bus)   LEPO; JPO; LEPO; JPO; LEPO;		İ			
and @ad < "20010830") and (disk storage) and (memory and controller and bus)) and processor) and DMA) and (memory near bus) USPAT; 2004/06/04 17:56 ((((((((((((((((((((((((((((((((((((	-	431	peripherall/mear.cobsrface)))	USPAT;	2004/06/04 14:43
and (memory and controller and bus)) and processor) and DMA) and (memory near bus)  ((((((((((1711/12).CCLS.) ((710/8,22-28,100,113,305,306,308,309,311,313) ((182185748); and @ad < "20010830") and (disk storage)) and (memory and controller and bus)) and processor) and DMA) and (memory near bus)  191		1		USCHKGP)UB;	
and (memory and controller and bus)) and processor) and DMA) and (memory near bus)  ((((((((((1711/12).CCLS.) ((710/8,22-28,100,113,305,306,308,309,311,313) ((182185748); and @ad < "20010830") and (disk storage)) and (memory and controller and bus)) and processor) and DMA) and (memory near bus)  191			and @ad < "20010830") and (disk storage))	EPO; JPO;	
296				DERWENT;	
((710/8,22-28,100,113,305,306,308,309,311,313)   INCHESPUB; and @ad < "20010830") and (disk storage)   DERWENT; and @ad < "20010830") and (disk storage)   DERWENT; and @(disk indisk			processor) and DMA) and (memory near bus)	IBM_TDB	
and @ad < "20010830") and (disk storage) and (memory and controller and bus)) and processor) and DMA) and (memory near bus) IBM TDB USPAT; (710/8,22-28,100,113,305,306,308,309,311,313) and @ad < "20010830") and (disk storage)) and (memory and controller and bus)) and processor) and DMA) and (memory near bus) USPAT;	-	296	(((((((((711/112).CCLS.)	USPAT;	2004/06/04 17:56
and (memory and controller and bus) and processor) and DMA) and (memory near bus)   IBM_TDB   USPAT;   (710/8,22-28,100,113,305,306,308,309,311,313)   IEGERSPUB;   EPO; JPO; and (memory and controller and bus)) and processor) and DMA) and (memory near bus)   DERWENT;   IBM_TDB   USPAT;   EPO; JPO;   DERWENT;   DERWENT;   IBM_TDB   USPAT;   EPO; JPO;   DERWENT;   DERWENT			((710/8,22-28,100,113,305,306,308,309,311,313)	LUSC-IPSG_P)U)B;	
Processor   and DMA   and (memory near bus)   IBM_TDB   (170/8,22-28,100,113,305,306,308,309,311,313   IBM_TDB   (1710/8,22-28,100,113,305,306,308,309,311,313   IBM_TDB   (1710/8,22-28,100			and @ad < "20010830") and (disk storage))	EPO; JPO;	
- 191					
((710/8,22-28,100,113,305,306,308,309,311,313)					
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